

Part Number: WP7113VW1C-AMT White

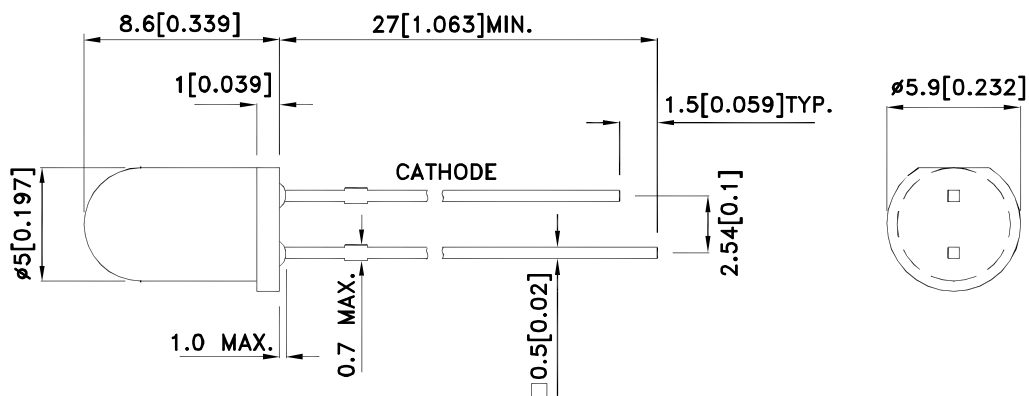


ATTENTION
OBSERVE PRECAUTIONS
FOR HANDLING
ELECTROSTATIC
DISCHARGE
SENSITIVE
DEVICES

Features

- High reliability LED package.
- Low power consumption.
- Popular T-1 3/4 diameter package.
- General purpose leads.
- Reliable and rugged.
- Long life - solid state reliability.
- Available on tape and reel.
- RoHS compliant.

Package Dimensions



Description

The source color devices are made with InGaN Light Emitting Diode.

Static electricity and surge damage the LEDs.

It is recommended to use a wrist band or anti-electrostatic glove when handling the LEDs.

All devices, equipment and machinery must be electrically grounded.

Applications

- Traffic signaling.
- Backlighting (illuminated advertising , general lighting).
- Interior and exterior automotive lighting.
- Substitution of micro incandescent lamps.
- Reading lamps.
- Signal and symbol luminaire for orientation.
- Marker lights (e.g. Steps, exit ways, etc).
- Decorative and entertainment lighting.
- Indoor and outdoor commercial and residential architectural lighting.

Notes:

1. All dimensions are in millimeters (inches).
2. Tolerance is $\pm 0.25(0.01)$ unless otherwise noted.
3. Lead spacing is measured where the leads emerge from the package.
4. The specifications, characteristics and technical data described in the datasheet are subject to change without prior notice.



Selection Guide

Part No.	Dice	Lens Type	Iv (mcd) [2] @ 20mA			Viewing Angle [1]
			Code.	Min.	Max.	2θ1/2
WP7113VW1C-AMT	White (InGaN)	Water Clear	ZG	8000	9000	20°
			ZH	9000	11000	
			ZM	11000	14000	

Notes:

- θ1/2 is the angle from optical centerline where the luminous intensity is 1/2 of the optical peak value.
- Luminous intensity/ luminous Flux: +/-15%.

Absolute Maximum Ratings at Ta=25°C

Parameter	Symbol	Value	Unit
Power dissipation	Pd	120	mW
Reverse Voltage	VR	5	V
Junction temperature	TJ	110	°C
Operating Temperature	Top	-40 To +100	°C
Storage Temperature	Tstg	-40 To +110	°C
DC Forward Current[1]	IF	30	mA
Peak Forward Current [2]	IFM	100	mA
Electrostatic Discharge Threshold (HBM)		250	V
Thermal Resistance (Junction/ambient) [1]	Rth j-a	400	°C/W
Lead Solder Temperature [3]		260°C For 3 Seconds	
Lead Solder Temperature [4]		260°C For 5 Seconds	

Notes:

- Rth(j-a) Results from mounting on PC board FR4 (pad size≥16 mm² per pad),
- 1/10 Duty Cycle, 0.1ms Pulse Width.
- 2mm below package base.
- 5mm below package base.

Electrical / Optical Characteristics at Ta=25°C

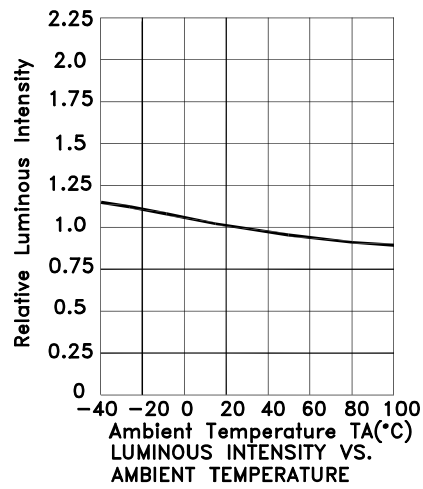
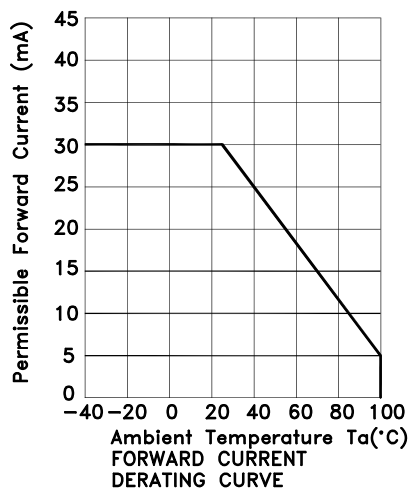
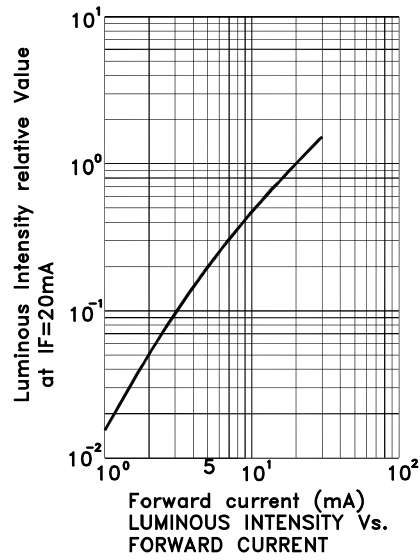
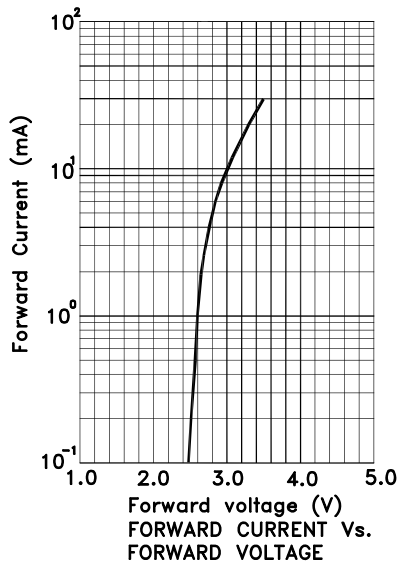
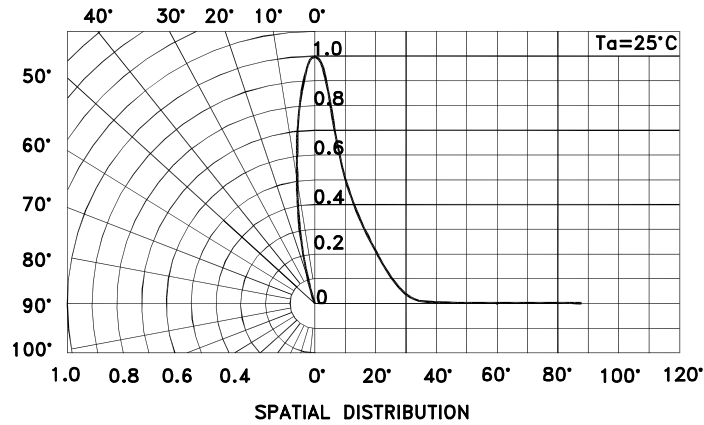
Parameter	Symbol	Value	Unit
Chromaticity coordinate x acc.to CIE1931 IF=20mA [Typ.]	x [1]	0.31	
Chromaticity coordinate y acc.to CIE1931 IF=20mA [Typ.]	y [1]	0.31	
Reverse Current (VR = 5V) [Max.]	IR	50	uA
Forward Voltage IF=20mA [Min.]	VF [2]	2.8	V
Forward Voltage IF=20mA [Typ.]		3.3	
Forward Voltage IF=20mA [Max.]		4.0	
Temperature coefficient of VF IF=20mA, -10 ° C ≤ T ≤ 100 ° C [Typ.]	TCv	-2.5	mV/° C
Temperature coefficient of x IF=20mA, -10 ° C ≤ T ≤ 100 ° C [Typ.]	TCx	-0.1	10 ⁻³ /° C
Temperature coefficient of y IF=20mA, -10 ° C ≤ T ≤ 100 ° C [Typ.]	TCy	-0.2	10 ⁻³ /° C

Notes:

- Measurement tolerance of the chromaticity coordinates is ±0.02.
- Forward Voltage: +/-0.1V.

White

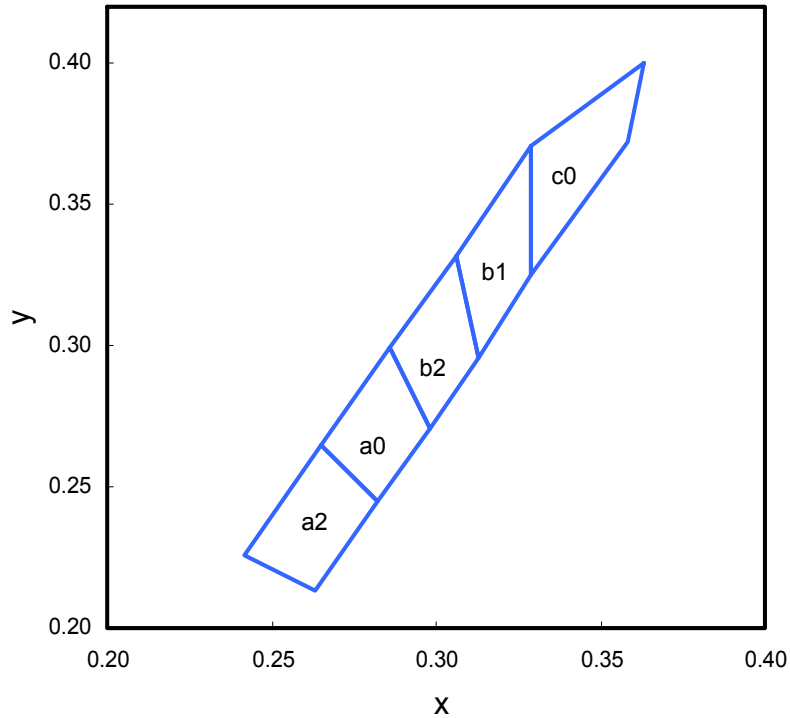
WP7113VW1C-AMT



White

WP7113VW1C-AMT

White CIE

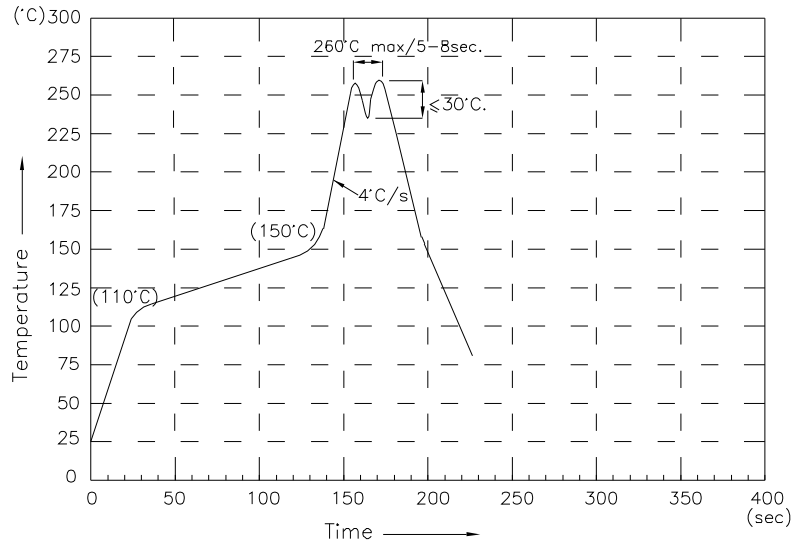


	x	y		x	y		x	y
a2	0.263	0.213	a0	0.282	0.245	b2	0.298	0.271
	0.282	0.245		0.298	0.271		0.313	0.296
	0.265	0.265		0.286	0.299		0.306	0.332
	0.242	0.226		0.265	0.265		0.286	0.299
b1	0.313	0.296	c0	0.329	0.325			
	0.329	0.325		0.358	0.372			
	0.329	0.371		0.363	0.400			
	0.306	0.332		0.329	0.371			

Notes:
 Shipment may contain more than one chromaticity regions.
 Orders for single chromaticity region are generally not accepted.
 Measurement tolerance of the chromaticity coordinates is ± 0.02 .

WP7113VW1C-AMT

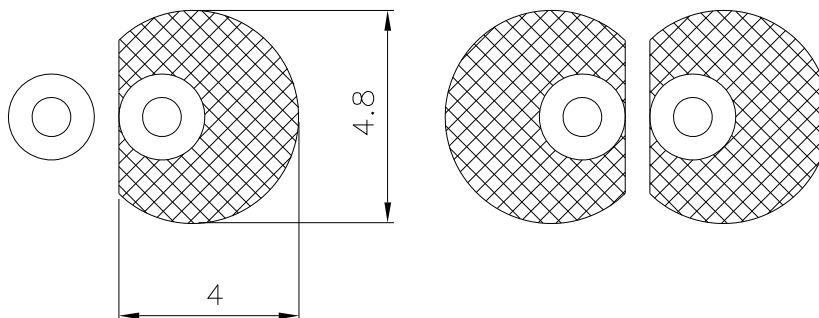
Wave Soldering Profile For Lead-free Through-hole LED.



NOTES:

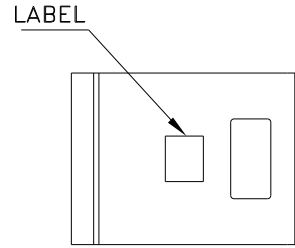
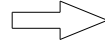
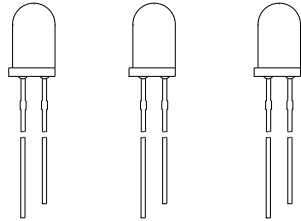
- 1.Recommend the wave temperature 245°C~260°C.The maximum soldering temperature should be less than 260°C.
- 2.Do not apply stress on epoxy resins when temperature is over 85 degree°C.
- 3.The soldering profile apply to the lead free soldering (Sn/Cu/Ag alloy).
- 4.No more than once.

Recommended Soldering Pattern (Units : mm; Tolerance: ±0.1)



PACKING & LABEL SPECIFICATIONS

WP7113VW1C-AMT

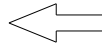


500PCS / BAG

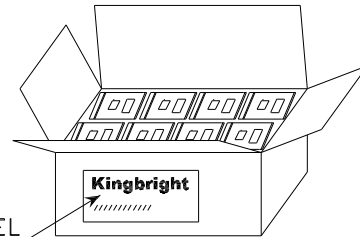


20K / 9# BOX

OUTSIDE LABEL



OUTSIDE LABEL



10K / 5# BOX

<h2 style="margin: 0;">Kingbright</h2>		
P/NO: WP7113xxx		
QTY: 500 pcs	Q.C.	<div style="border: 1px solid black; border-radius: 50%; width: 30px; height: 30px; margin: 0 auto; display: flex; align-items: center; justify-content: center;"> <div style="text-align: center; margin-bottom: 2px;">Q C</div> <div style="font-size: 8px; margin-bottom: 2px;">XX XX XXXX</div> <div style="font-weight: bold; font-size: 8px;">PASSED</div> </div>
S/N: XXXX		
CODE: XXX		
LOT NO:		
<small>XXXXXXXXXXXXXXXXXXXXXXXXXXXX</small> RoHS Compliant		

Reliability Test Items And Conditions

The reliability of products shall be satisfied with items listed below

Lot Tolerance Percent Defective (LTPD) : 10%

No.	Test Item	Standards	Test Condition	Test Times / Cycles	Number of Damaged
1	Continuous operating test	-	Ta = 25°C ,IF = maximum rated current*	1,000 h	0 / 22
2	High Temp. operating test	EIAJ ED-4701/100(101)	Ta = 100°C IF = maximum rated current*	1,000 h	0 / 22
3	Low Temp. operating test	-	Ta = -40°C, IF = maximum rated current*	1,000 h	0 / 22
4	High temp. storage test	EIAJ ED-4701/100(201)	Ta = maximum rated storage temperature	1,000 h	0 / 22
5	Low temp. storage test	EIAJ ED-4701/100(202)	Ta = -40°C	1,000 h	0 / 22
6	High temp. & humidity storage test	EIAJ ED-4701/100(103)	Ta = 60°C, RH = 90%	1,000 h	0 / 22
7	High temp. & humidity operating test	EIAJ ED-4701/100(102)	Ta = 60°C, RH = 90% IF = maximum rated current*	1,000 h	0 / 22
8	Soldering reliability test	EIAJ ED-4701/100(301)	Moisture soak : 30°C,70% RH, 72h Preheat : 150~180°C(120s max.) Soldering temp : 260°C(10s)	3 times	0 / 18
9	Thermal shock operating test	-	Ta = -40°C(15min) ~ 100°C(15min) IF = derated current at 100°C	1,000 cycles	0 / 22
10	Thermal shock test	-	Ta = -40°C(15min) ~ maximum rated storage temperature(15min)	1,000 cycles	0 / 22
11	Electric Static Discharge (ESD)	EIAJ ED-4701/100(304)	C = 100pF , R2 = 1.5KΩ V = 250V	Once each Polarity	0 / 22
12	Vibration test	-	a = 196m/s ² , f = 100~2KHz , t = 48min for all xyz axes	4 times	0 / 22

* : Refer to forward current vs. derating curve diagram

Failure Criteria

Items	Symbols	Conditions	Failure Criteria
luminous Intensity	Iv	IF = 20mA	Testing Min. Value < Spec.Min.Value x 0.5
Forward Voltage	Vf	IF = 20mA	Testing Max. Value ≥ Spec.Max.Value x 1.2
Reverse Current	IR	VR = Maximum Rated Reverse Voltage	Testing Max. Value ≥ Spec.Max.Value x 2.5
High temp. storage test	-	-	Occurrence of notable decoloration, deformation and cracking

LED MOUNTING METHOD

1. The lead pitch of the LED must match the pitch of the mounting holes on the PCB during component placement. Lead-forming may be required to insure the lead pitch matches the hole pitch. Refer to the figure below for proper lead forming procedures.

(Fig. 1)

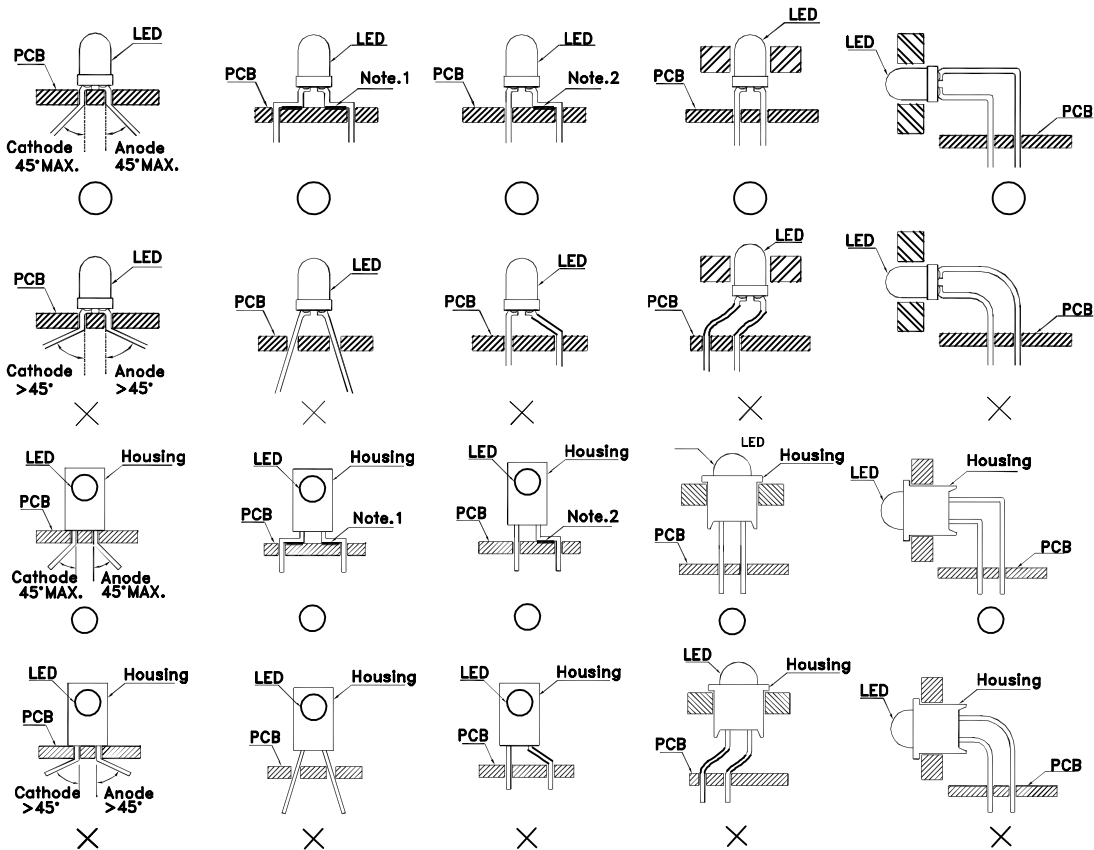


Fig.1

“○” Correct mounting method “×” Incorrect mounting method
 Note 1-2 : Do not route PCB trace in the contact area between the leadframe and the PCB to prevent short-circuits.

2. When soldering wire to the LED, use individual heat-shrink tubing to insulate the exposed leads to prevent accidental contact short-circuit.
 (Fig. 2)

3. Use stand-offs (Fig. 3) or spacers (Fig. 4) to securely position the LED above the PCB.

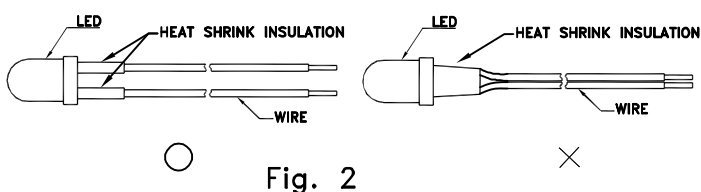


Fig. 2

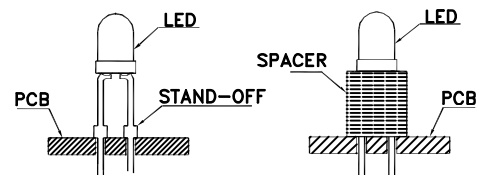
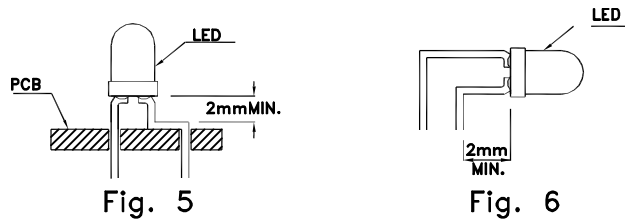


Fig. 3

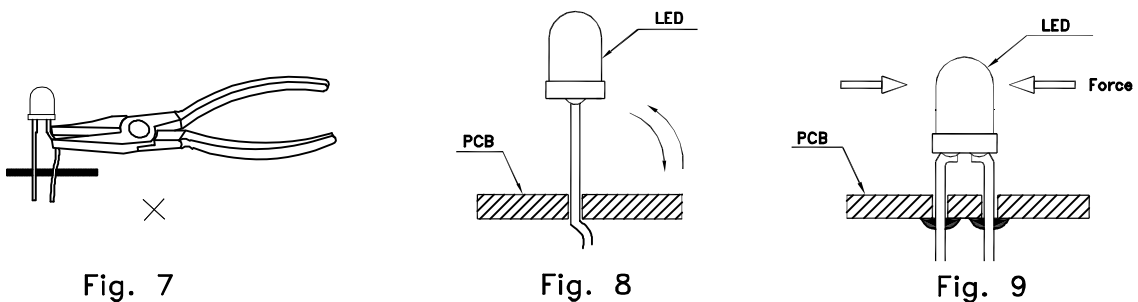
Fig. 4

LEAD FORMING PROCEDURES

1. Maintain a minimum of 2mm clearance between the base of the LED lens and the first lead bend. (Fig. 5 and 6)



2. Lead forming or bending must be performed before soldering, never during or after Soldering.
3. Do not stress the LED lens during lead-forming in order to fractures in the lens epoxy and damage the internal structures.
4. During lead forming, use tools or jigs to hold the leads securely so that the bending force will not be transmitted to the LED lens and its internal structures. Do not perform lead forming once the component has been mounted onto the PCB. (Fig. 7)
5. Do not bend the leads more than twice. (Fig. 8)
6. After soldering or other high-temperature assembly, allow the LED to cool down to 50°C before applying outside force (Fig. 9). In general, avoid placing excess force on the LED to avoid damage. For any questions please consult with Kingbright representative for proper handling procedures.



7. No stress shall be applied on the LED during soldering to prevent damage.

